

# Lecture 35: C Language “Features”

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## Goto

- Go directly to a label
- Also known as “Progenitor of 1000 Bugs”
- Your processor only has `goto`

## Thanksgiving

- Last week’s assignment is due today
  - Resources for help will be limited after noon on Wednesday
- No lab or assignment this week
- Graphs next week

#NoDAPL

## Goto Example

```
int dont_use_goto(int x) {
    one:
        x += 5;
    two:
        x += 3;
        if (x < 12) goto three;
        return x;
    three:
        if (x % 8) goto two;
        goto one;
}
```

## Break and Continue

- `break` gets you out of a single loop.
  - Everyone knows where you're going
  - Can still be confusing
- `continue` takes you to the start of the current loop

No easy way to break out of 2+ loops at once

## Goto Example

```
int lazy_sum(int *ar, int wdhth, int hght) {  
    int sum = 0;  
    for(int x = 0; x < wdhth; ++x) {  
        for(int y = 0; y < hght; ++y) {  
            sum += ar[x+y*wdhth];  
            if (sum >= 30) {  
                goto done;  
            }  
        }  
    }  
    return sum;  
done:  
    return 30;  
}
```

## Unions

- Did you ever want to declare two data structures that shared the same memory?
- Imagine threads, but for memory
  - Yup.
- Don't use them.

## Union Example

```
union int_and_float_u {  
    int i;  
    float f;  
};  
typedef union int_and_float_u int_and_float;
```

What happens if we set `i` or `f`?

## Unions

- Multiple ‘views’ of the same data
- Used for data manipulation and either/or situations

## Example

```
myprogram: myprogram.c myprogram.h  
        clang myprogram.c myprogram.h -o myprogram
```

Can use wildcards:

```
% .exe: %.c %.h  
        clang $< -o $@
```

## Makefiles

- Contain *rules*:

```
target: dependencies  
        commands to build target  
        from dependencies
```

- Checks modification time of resources
- Figures out build order
- Commands are just shell commands

## Makefile Variables

- Variables store common configuration
- Use `$()` syntax to get value
- Often used for common arguments, e.g., how to compile

## Our Makefile