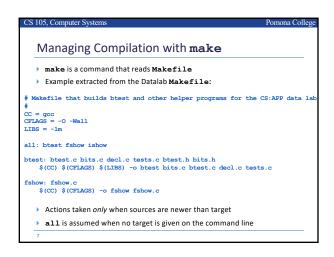
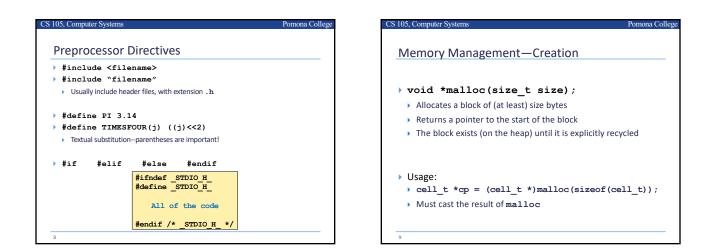


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gcc a	nd File Types	
Some files from	m the Datalab:	
Makefile		
README		
bits.c	Source file	
bits.h	Include file	
bits.o	Object file	gcc -c bits.c
bits.s	Assembly listing	gcc -S bits.c
btest	Executable program	gcc -o btest btest.c bits.o decl.o
btest.c		
btest.h		
btest.o		
dlc		
6		



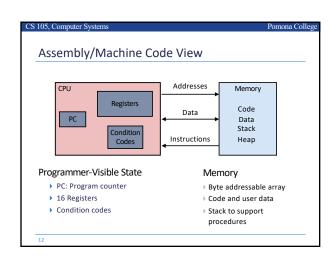


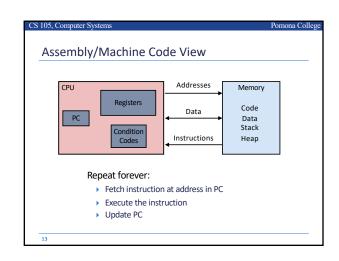
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## 

CS 105. Computer Systems

## CS 105, Computer Systems Pomona Colleg New Topic: X64 Assembly Language Intel Pentium: 64 bit instruction set Evolutionary design, going back to 8086 in 1978 Basis for original IBM Personal Computer, 16-bits Other languages are translated into X64 instructions and then executed on the CPU Actual instructions are sequences of bytes We give them mnemonic names





	Integer Regi		
%rax	%eax	%r8	%r8d
%rbx	%ebx	%r9	%r9d
%rcx	%ecx	%r10	%r10d
%rdx	%edx	%r11	%r11d
%rsi	%esi	%r12	%r12d
%rdi	%edi	%r13	%r13d
%rsp	%esp	%r14	%r14d
%rbp	%ebp	%r15	%r15d

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X86-64 Register Usage Conventions					
<pre>%rax, function result</pre>	%r8				
%rbx	%r9				
<pre>%rcx, fourth argument</pre>	%r10				
<pre>%rdx, third argument</pre>	%r11				
<pre>%rsi, second argument</pre>	<mark>%r12</mark>				
<pre>%rdi , first argument</pre>	<mark>%r13</mark>				
<pre>%rsp, stack pointer</pre>	<mark>%r14</mark>				
%rbp	<mark>%r15</mark>				
Callee-saved registe	Callee-saved registers are in yellow				
15					

## Assembly Characteristics: Data Types

- "Integer" data of 1, 2, 4, or 8 bytes
- Data values

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- Addresses (untyped pointers)
- Floating point data of 4, 8, or 10 bytes
- Code: Byte sequences encoding series of instructions
- No aggregate types such as arrays or structures
- Just contiguously allocated bytes in memory

16

## Assembly Characteristics: Operations

Perform arithmetic function on register or memory data

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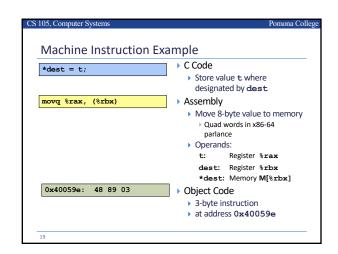
- Transfer data between memory and register
  - Load data from memory into register
  - Store register data into memory
- Transfer control

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- Unconditional jumps to/from procedures
- Conditional branches

	popq ret	%rax, (%rbx) %rbx
,	. ,	vith command
ces the file <b>sum.s</b>		
	cc -Og -S sum	assembly listing (on pom-itb-cs2) v cc -Og -S sum.c ces the file sum.s et very different results on different



Disassembling Object Code		Alternate Disass
000000000400595 <sumstore>:           400595:         53         pu           400596:         48         89         d3         mo           400599:         e8         f2         ff         ff         ca           400599:         e8         89         03         mo           400594:         48         89         03         mo           400554:         5b         po         4005a1:         5b</sumstore>	v %rdx,%rbx 11q 400590 <plus> v %rax,(%rbx) p %rbx</plus>	Dump of assembl. 0x00000000040 0x0000000040 0x00000000040 0x00000000
Disassembler		Using the g
\$ objdump -d sum	\$ gdb su	
<ul> <li>Useful tool for examining object code</li> </ul>	(gdb) di	
<ul> <li>Analyzes bit pattern of series of instruct</li> </ul>	(gdb) x/	
<ul> <li>Produces approximate rendition of assert</li> </ul>	Examin	
Can be run on either a.out (complete	executable) or . o file	

