

CS 105, Computer Systems Pomona College
Mentor Sessions

- Monday, Wednesday, and Thursday 7-9 pm
- Sunday 3-5 pm
- In Edmunds 105 ... or Edmunds 219



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| Example Data Representations |  |  |  |  |
| C Data Type | Typical 32-bit | Typical 64-bit | $\times 86-64$ |  |
| char | 1 | 1 | 1 |  |
| short | 2 | 2 | 2 |  |
| int | 4 | 4 | 4 |  |
| long | 4 | 8 | 8 |  |
| long long | 8 | 8 | 8 |  |
| float | 4 | 4 | 4 |  |
| double | 8 | 8 | 8 |  |
| pointer | 4 | 8 | 8 |  |
| 5 |  |  |  |  |



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Examining Data: The Result
pom-itb-cs2:tmp 16\$ gcc -o showbytes showbytes.c
pom-itb-cs2:tmp 17\$ ./showbytes
int $a=15213$.
int $a=0 \times 00003 b 6 d$;
$0 \times 7 f f f 5 e 1 f 7 b 48 \quad 0 \times 6 \mathrm{~d}$
$0 \times 7 f f f 5 e 1 f 7 b 49 \quad 0 \times 3 b$
$0 x 7 f f f 5 e 1 f 7 b 4 a \quad 0 x 00$
$0 x 7 f f f 5 e 1 f 7 b 4 b \quad 0 x 00$
pom-itb-cs2:tmp 18\$

Reading "Byte-reversed" listings

- A debugger converts binary machine code into assembly language. Here is a fragment with an embedded constant.

| Address | Instruction Code | Assembly Rendition |  |  |
| :--- | :--- | :--- | :--- | :--- |
| 8048365: | 5 b |  |  | pop | \%ebx

x x86 instructions are between 1 and 15 bytes long. Other processors have fixed-length instructions
, Use w-bit words; w can be $8,16,32$, or 64

* The bit sequence $b_{w-1} \ldots b_{1} b_{0}$ represents an integer

- Important!! "signed" does not mean "negative"

Example: Three-bit integers

## Arithmetic, Part 1

- Usual addition and subtraction
- Like you learned in second grade, only binary
- Same for unsigned and signed
, ... but error conditions differ
- To negate a signed value: complement the bits and add 1 Reason: $x+\sim x=11 \ldots 1=-1$, so $x+(\sim x+1)=0$

Flags

- A flag is a one-bit value: 1 is "set" and 0 is "unset"
- Flags record conditions of previous arithmetic operations
- C: The carry-out flag from the last bit; indicates unsigned overflow
- Z: Set if the result is zero
- $\mathbf{N}$ : The sign bit of the result; indicates a negative signed result
- V: Indicates if the result, interpreted as a signed value, is erroneous. For addition, this means that the signs of the operands agree and the result has a different sign


## Arithmetic, Part 2

- Comparisons: <, <=, ==, !=, >=, >

Return "logical values, 0 or 1

- Computation relies on subtraction and flags
, Different for unsigned and signed
- Multiplication
- Product can be two words long; it may be truncated to one word
- Different for unsigned and signed
- Division

Produces quotient and remainder, one word each

- Different for unsigned and signed
- In $\times 86$, the (signed) remainder has the same sign as the numerator


## Casting Types in C

- "Casting" means changing the type of a value

```
sometype x;
othertype y;
x = y; // type error!
x = (sometype) y;
```

- Sometimes it means "interpret these bits in a different way" - Unsigned to/from signed
- Other times it means "convert these bits to the same value in a different representation"
- Shorter integer types to/from longer
- Integer types to/from floating point

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## Integer Types in C

- All integer types (char, short, int, long) can be prefixed with unsigned
- Constants are, by default, signed. Unsigned constants have the suffix $U$
- Casting between unsigned and signed changes the interpretation, but not the bits
- Implicit casting occurs in assignments and parameter lists. In mixed expressions, signed values are implicitly cast to unsigned

Source of many errors!
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Sign Extension

|  | 5 | -5 |
| :--- | ---: | ---: |
| four bits <br> eight bits | 0101 | 1011 |
|  | 00000101 | 11111011 |
|  |  |  |

- To convert a signed value to a larger number of bits, simply replicate the sign bit on the left.
Reason: $-b 2^{w-1}+$ other stuff $=-b 2^{w}+b 2^{w-1}+$ other stuff
Multiplying with Shifts

C uses << and >>. The arithmetic/logical choice is made according the the operands being signed/unsigned.

Java has no unsigned integers, but it has a third shift >>> for logica right shift.

We can multiply (often faster than with the processor's multiply instruction) with shifts

$$
\text { - } \begin{aligned}
x \times 24 & =x \times 32-x \times 8 \\
& =(x \ll 5)-(x \ll 3)
\end{aligned}
$$

Most compilers will generate this code automatically.

Signed Division by a Power of 2
| $\mathbf{x}>\mathbf{k}$ computes $\mathbf{x} / \mathbf{2}^{\mathbf{k}}$, rounded towards $-\infty$

- C on Intel processors rounds towards 0
| -11 >> 2 == -3, but $-11 / 4==-2$
- Solution: If $x<0$, add $2^{k}-1$ before shifting
- Why does this work?

```
if (x < 0)
    x += (1 << k) - 1;
return x >> k;
```

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- Rarely
- When doing multi-precision arithmetic, or when you need an extra bit of range ... but be careful!
unsigned $i$;
for ( $i=c n t-2$; $i>=0$; $i--$ ) $a[i]+=a[i+1]$;

Pointer Arithmetic in C
Arrays

- Pointers are, effectively, unsigned integers that signify addresses in memory
- Suppose $\mathbf{p 0}$ and $\mathbf{p 1}$ are pointers to type $\mathbf{T}$, and j is an integer
- p0 - p1 is a signed value; it is the number of objects (not bytes!) between the two addresses
- $\mathrm{p} 0+\mathrm{p} 1$ and p 0 * p 1 are disallowed
- $\mathrm{pO}+\mathrm{j}$ means $\mathrm{p} 0+j$ * sizeof( T )
- $\& \mathbf{x}$ is the address where $\mathbf{x}$ is stored
- Arrays are implemented as pointers.

Contiguous block of memory

- Pointer to start, then indexed by element size
- Indices start at zero
bary [k] is the same as * (ary+k)

Two-dimensional Arrays
Typedefs
b bil][j] is the same as $b[8 * i+j]$

- Same storage layout:
int a[48]; // 48 integers int b[6][8]; // 6 rows, 8 columns
- "row major order"
- Abbreviation for complex types
int b[6][8]; // b is a two-dim array
typedef int b_type[6] [8];
b_type b_var; // b_var is a two-dim array

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What is printed?

```
int a[100];
int *p[47];
p[3] = a+12;
for (int i = 0; i < 100; i++)
    a[i] = i;
printf("%d\n", p[3][4]);
```




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| Structs |  |
| - Hetrogeneous records |  |
| Example: ```typedef struct cell { int value; struct cell *next; } cell t;``` |  |
| Usage | How many bytes are allocated for c ? for p ? |
| $\begin{array}{ll} \text { Usage with pointers: } & \begin{array}{l} \text { cell t }{ }^{*} \mathrm{p} ; \\ \mathrm{p}->\text { value }=42 ; \\ \mathrm{p}->\text { next }=\text { NULL; } \end{array} \end{array}$ |  |
| 30 |  |



